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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a CON of 09/406,664 09/27/1999 ABN ✓OK MT(1)

\*\* FOREIGN APPLICATIONS \*\*\*\*\* none MT(1)

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u> <u>01-41</u>	STATE OR COUNTRY TX	SHEETS DRAWING 6	TOTAL CLAIMS 45	INDEPENDENT CLAIMS 5
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 23389  
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 11530 ✓OK MT(1)

TITLE  
 Method for VLSI system debug and timing analysis

FILING FEE  RECEIVED 1368	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
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